



Attorney Docket No.: 64766-015  
PATENT

Jeff

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of  
Katsumasa HIJIKATA, et al.  
Application No.: 10/581,038  
Filed: May 30, 2006  
For: MIXER CIRCUIT

Customer No. 53080  
Confirmation No.: 6906  
Group Art Unit: 2618  
Examiner: PABLO N. TRAN

ELECTION UNDER 35 U.S.C. § 121

Mail Stop Amendment  
Hon. Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the restriction requirement set forth in the Office Action mailed October 20, 2008, having a shortened statutory period for response set to expire November 20, 2008, wherein the Examiner required restriction between the following distinct Species:

**Group I** - Claims 1, 2, 5, 6, 14-17, 19 and 21, drawn to a single balanced mixer having first and second bypass current sources connected in parallel with first and second LO transistors, respectively (see Fig. 1);

**Group II** - Claims 1, 3, 5, 7, 14-17, 19 and 21, drawn to a single balanced mixer having a bypass current source supplying bias current only to the RF signal transistor (see Fig. 4);

**Group III** - Claims 1, 4, 5, 8, 14-17, 19 and 21, drawn to a single balanced mixer having a bypass current source supplying bias current only to the RF signal transistor and bypass current sources supplying bias current to output load resistors (see Fig. 6);

**Group IV** - Claims 1, 2, 9, 10, 14-16, 18, 20 and 22, drawn to a double balance mixer having first, second, third, and fourth bypass current supplies connected in parallel with first, second, third, and fourth LO transistors, respectively (see Fig. 8);

**Group V** - Claims 1, 2, 9, 11, 14-16, 18, 20 and 22, drawn to a double balance mixer having a first bypass current source connected in parallel with the first LO transistor and a second bypass current source connected in parallel with the fourth LO transistor (see Fig. 10);

**Group VI** - Claims 1, 3, 9, 12, 14-16, 18, 20 and 22, drawn to a double balance mixer having first and second bypass current sources supplying bias current only to the first and second RF signal transistor, respectively (see Fig. 12); and

**Group VII** - Claims 1, 4, 9, 13, 14-16, 18, 20 and 22, drawn to a double balanced mixer having first and second bypass current sources supplying bias current only to the first and second RF signal transistor, respectively, and bypass current sources supplying bias current to output load resistors (see Fig. 14).

Applicants elect **Group II**, with claims 1, 3, 5, 7, 14-17, 19 and 21 readable thereon, for initial prosecution on the merits. Applicants also reserve the right to file a Divisional Application for the non-elected claims which the Examiner has indicated are patentably distinct.

Applicants believe that no extension of time is required. However, this conditional petition is being made to provide for the possibility that Applicants have inadvertently overlooked the need for a petition for extension of time.

The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No. 50-0417.

Respectfully submitted,

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Date: 11/20/08

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